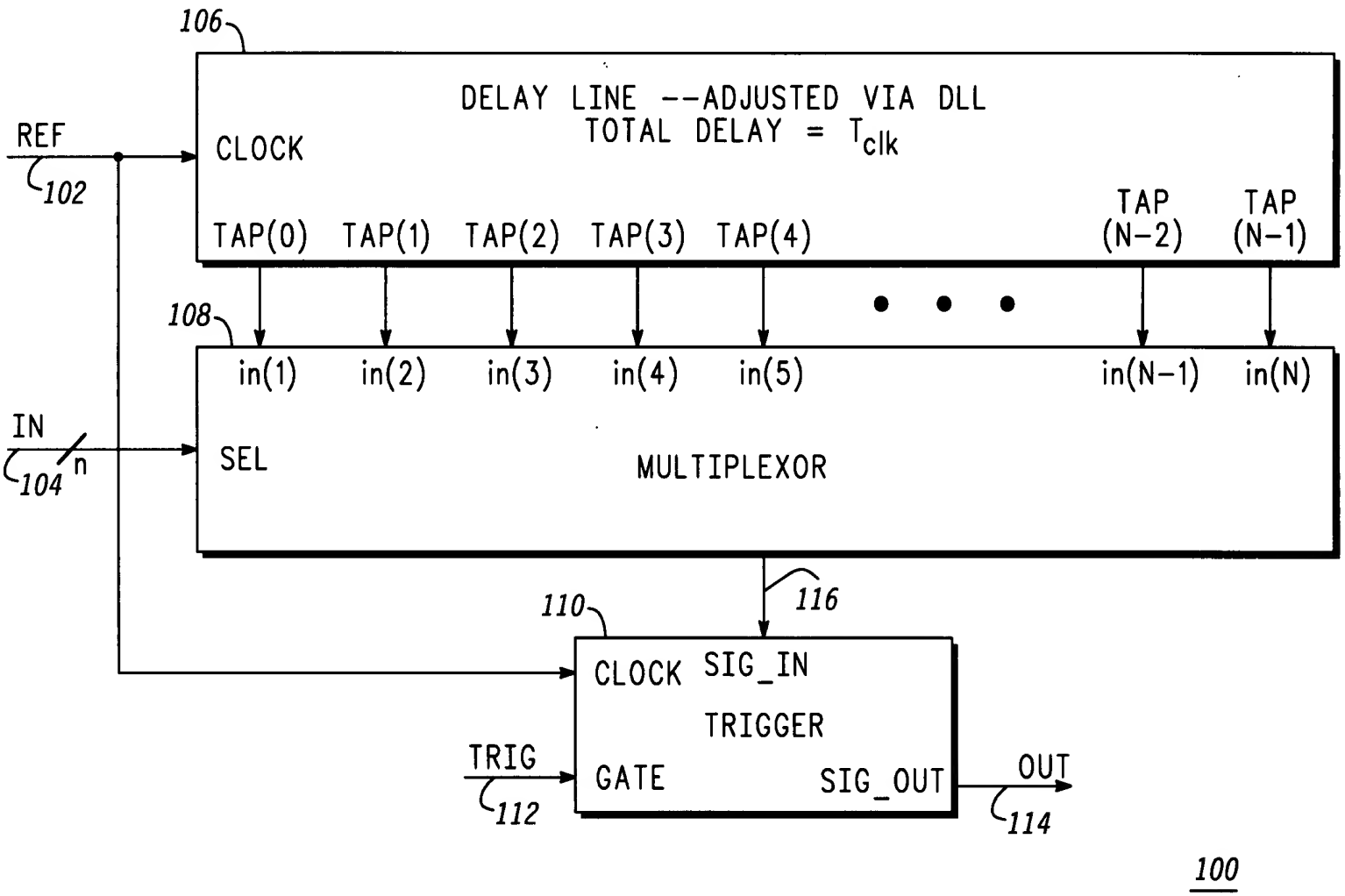
106
DELAY LINE --ADJUSTED VIA DLL
TOTAL DELAY = T_{clk}
REF
102
CLOCK
TAP(0)
TAP(1)
TAP(2)
TAP(3)
TAP(4)
TAP (N-2)
TAP (N-1)
108
in(1)
in(2)
in(3)
in(4)
in(5)
in(N-1)
in(N)
IN
104
n
SEL
MULTIPLEXOR
116
110
CLOCK
SIG_IN
TRIGGER
TRIG
112
GATE
SIG_OUT
OUT
114
100

FIG. 1

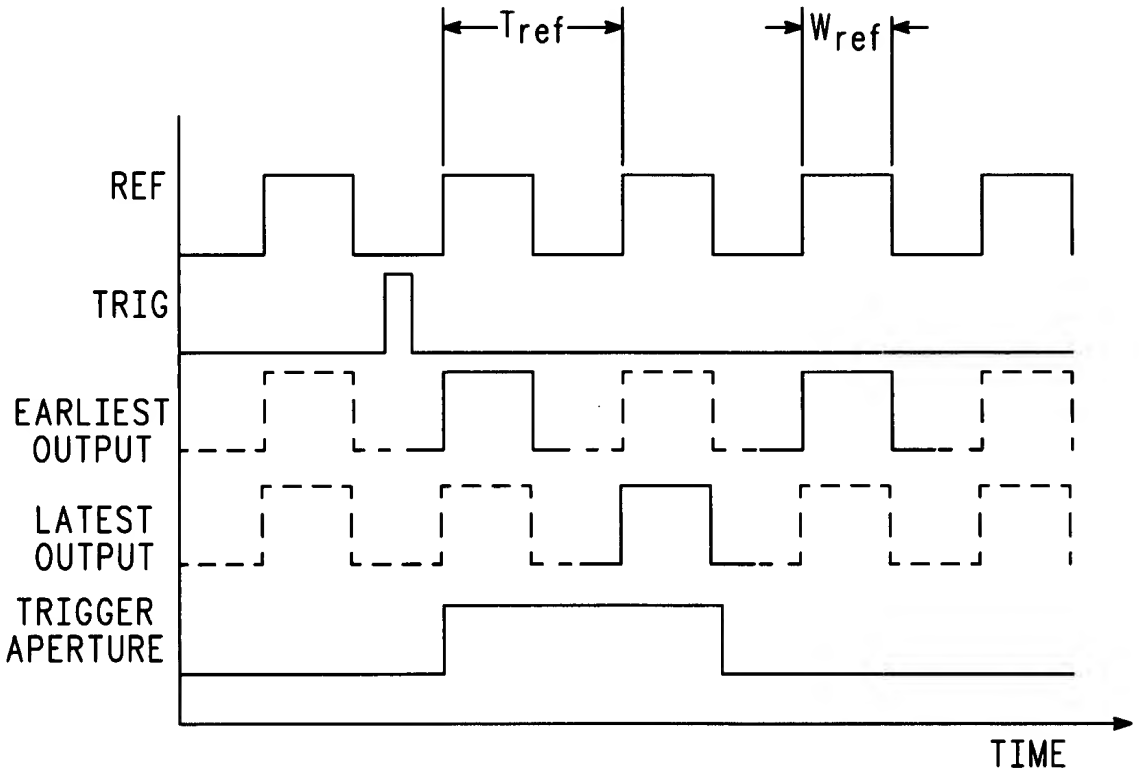
Tref
Wref
REF
TRIG
EARLIEST OUTPUT
LATEST OUTPUT
TRIGGER APERTURE
TIME

FIG. 2

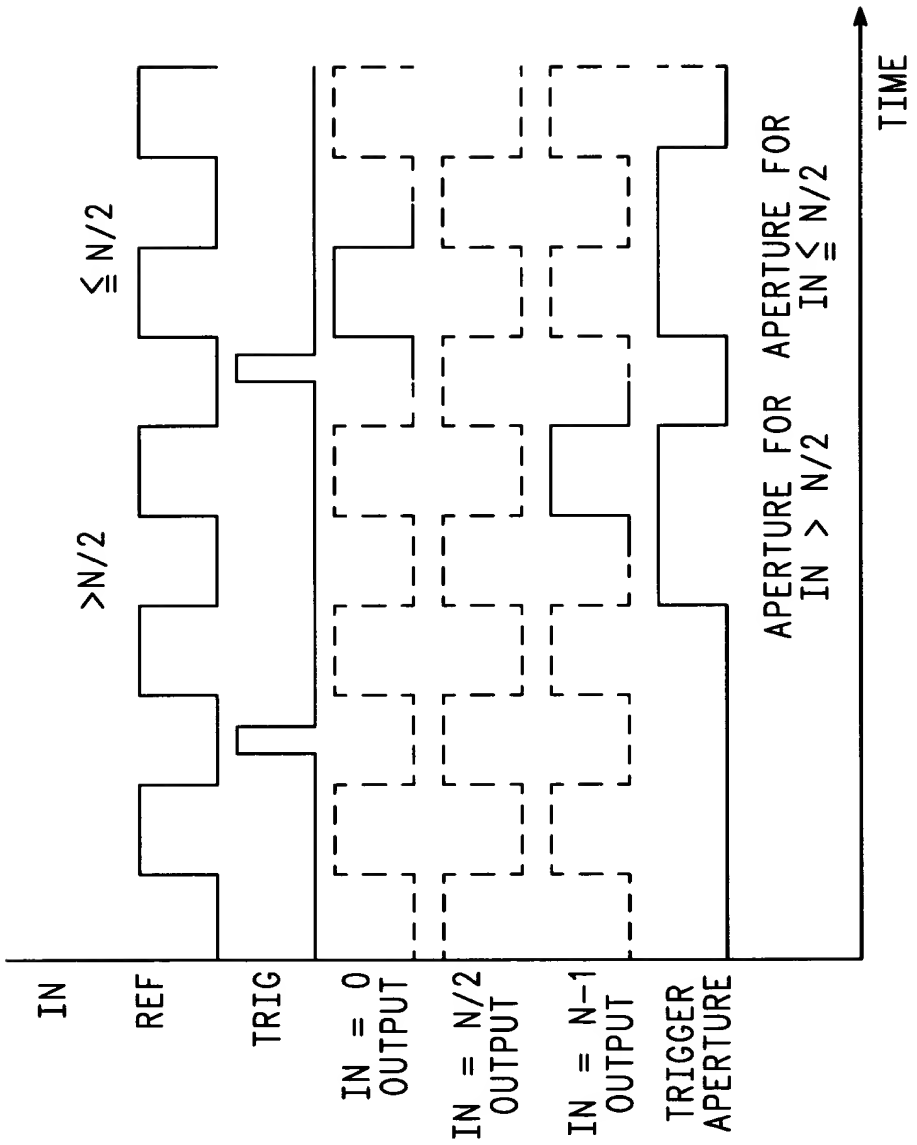
IN
REF
TRIG
IN = 0 OUTPUT
IN = N/2 OUTPUT
IN = N-1 OUTPUT
TRIGGER APERTURE
>N/2
≦N/2
APERTURE FOR IN > N/2
APERTURE FOR IN ≦ N/2
TIME

FIG. 3

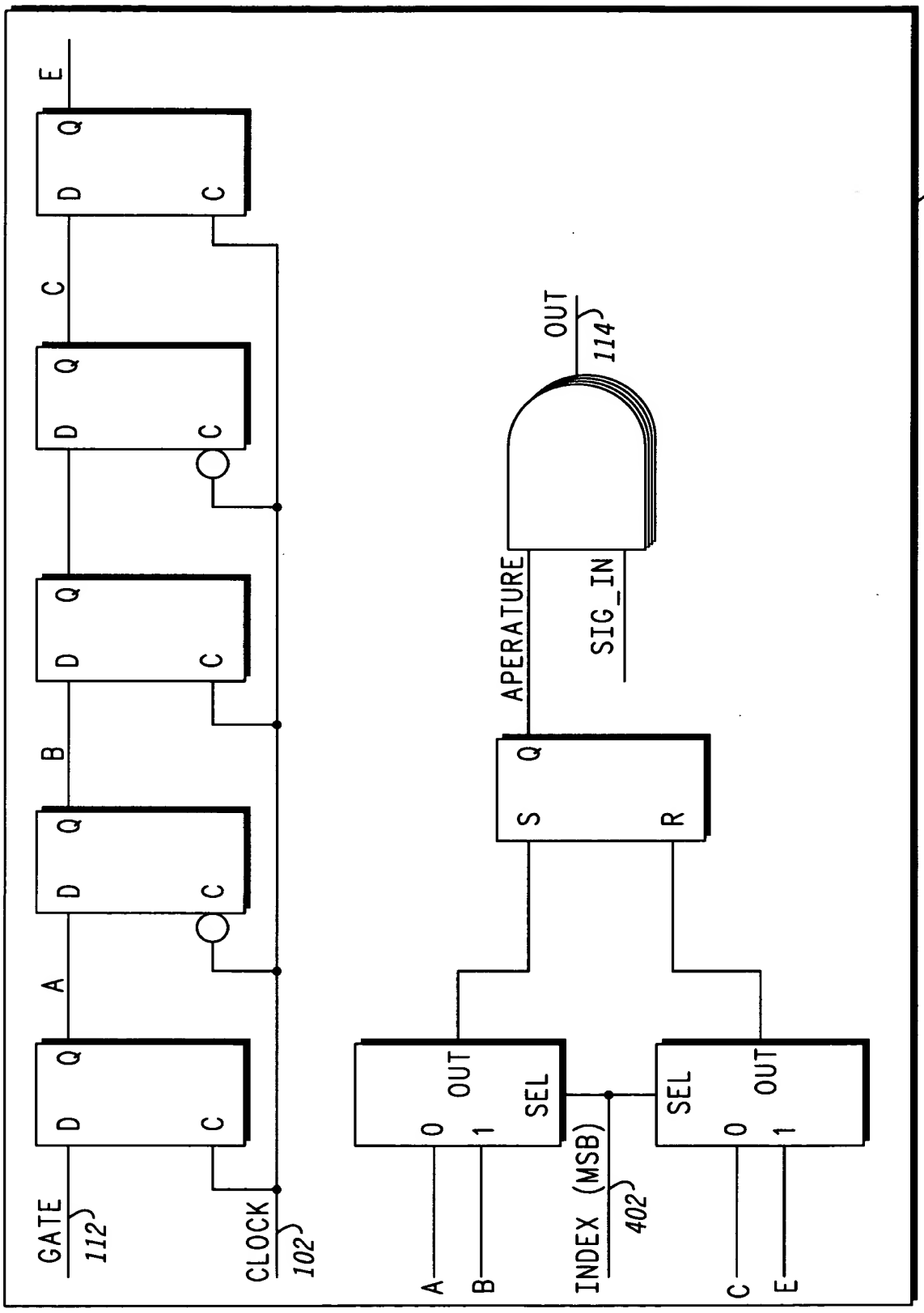
400
GATE
112
CLOCK
102
A
B
C
E
D
Q
C
OUT
114
APERATURE
SIG_IN
S
R
0
1
SEL
INDEX (MSB)
402

FIG. 4